

INFORMATION DISCLOSURE
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10/628,578

Applicant

LÜPPERT et al

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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	Sommer et al, "Analog Insydes 2 - New Features and Applications in Circuit Design"
	Halfman et al, "Behavioral Modeling and Transient Analysis With Analog Insydes"
	Sommer et al, "A Generic Circuit Modeling Strategy Combining Symbolic and Numeric Analysis", ITWM (Institute of Industrial Mathematics, Kaiserslautern, Germany. 1998
	Halfmann et al, "A Numerical Method to Synthesize the Element Characteristic in Analog Circuit Design"
	Halfmann et al, "Application of Symbolic Circuit Analysis: An Overview and Recent Results in Nonlinear Model Generation"
	Lutovac et al, "A MATLAB Toolbox for Analysis of Continuous-Time Filters", PROC 22 nd International Conference on Microelectronics (MIEL 2000), Vol. 2, NIS, SERBIA, 14-17 May 2000
	Lutovac et al, "AFDESIGN - A Toolbox for Continuous-Time Filter Design in MATLAB"
	Lutovac et al, "Symbolic Analysis of Programmable Digital Filters"
	Lutovac et al, "Digital Filter Design Using MATLAB and Mathematics"
	Tošić et al, "A Tool for Symbolic Analysis and Design of Analog Active Filters"

*Examiner		Date Considered	
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